

IN THE

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:

Vigyan Singhal et al.

SERIAL NO.:

10/606,419

FILING DATE:

June 26, 2003

TITLE (AS AMENDED):

System and Method for Guiding and Optimizing Formal

Verification for a Circuit Design

EXAMINER:

Tuyen P. To

GROUP ART UNIT:

2825

ATTY. DKT. NO.:

24006-08742

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below:

Dated:

6 In 2006

By:

John T. McNelis, Reg. No. 37,186

MAIL STOP AMENDMENT COMMISSIONER FOR PATENTS P.O. BOX 1450

ALEXANDRIA, VA 22313-1450

RESPONSE AND AMENDMENT "A" UNDER 37 CFR §1.111

Sir:

In response to the Office Action dated July 12, 2005, Applicant submits the following

Amendment and Remarks.

01/09/2006 NNGUYEN1 00000091 10606419

02 FC:2201

1500.00 OP

It is not believed that extensions of time are required beyond those, which may otherwise be provided for in documents accompanying this Amendment. However, in the event that additional extensions of time are necessary to prevent abandonment of this application, then such extensions of time are hereby petitioned under 37 CFR §1.136(a), and any fees required therefor are hereby authorized to be charged to our Deposit Account 19-2555.

Kindly enter the following amendment:

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IN THE TITLE

Please amend the title to: "System and Method for Guiding and Optimizing Formal Verification for a Circuit Design"